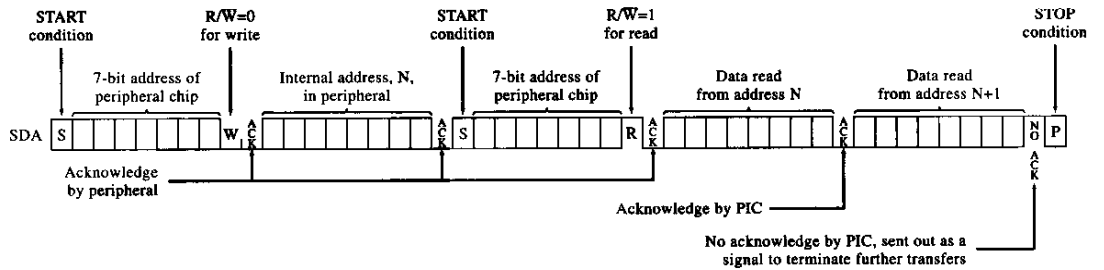(a) General format to write to several peripheral internal registers or addresses.

(b) General format to read from several peripheral internal registers or addresses.

Figure 9-5 I²C typical message string formats.

```
        bst    STATUS,RP0
```

then changing the required bit of **TRISC**, and finally reverting back to Bank 0 with

```
        bcf    STATUS,RP0
```

Instead of doing this, load the indirect pointer, **FSR**, with the address of **TRISC** and then do the required bit setting and bit clearing of **TRISC** bits indirectly. For example, with the following definitions

```
    SCL    equ    3
```

and

```
    SDA    equ    4
```

then

```
        bsf    INDF,SDA
```

will release the SDA line, letting the external pullup resistor of Figure 9-1b pull it high or some other I²C chip pull it low.